

Analysis of Power Quality Enhancement by DSTATCOM and DVR in Islanded Microgrid

Sagar Yadav, *Vikas Verma*, Himanshu Bhushan and Sachin Singh

Department of Electrical Engineering, Institute of Engineering and Technology, Lucknow, India

DOI: <https://doi.org/10.51583/IJLTEMAS.2026.150100070>

Received: 16 January 2026; Accepted: 21 January 2026; Published: 09 February 2026

ABSTRACT

In this paper, a Microgrid has been developed consist of renewable energy resources in MATLAB Simulink environment. First, a DVR has been modeled and used in the developed microgrid model. Power quality analysis is carried out with the help of simulation results considering the effect of Dynamic Voltage Restorer (DVR). Also, a MATLAB Simulink model of the DSTATCOM has been developed to compare its performance with DVR. The same DSTATCOM has been used with the earlier developed microgrid model to test its effectiveness. The performance of both DVR and DSTATCOM has been compared in common scenarios related to power quality issues and results are analyzed.

Keywords-DVR (Dynamic voltage restorer) DSTATCOM (Distribution Static synchronous compensator), THD, Power quality, Harmonics, Micro-grid.

INTRODUCTION

An essential advancement in the electrical power system is power generation. Modern society is entirely reliant on the electricity produced by generating plants. The three components of the traditional power system are the generation, transmission, and distribution of alternating current (AC) electricity. The power quality of the system is decreased due to excessive loads, unusual circumstances, or problems on the line, and it is very important to improve its quality [1]. As traditional sources of energy regularly run out, society is forced to consider additional alternatives that can meet the rising need for energy. One such option is the formation of a microgrid using small-scale renewable energy sources to supply a local area. The two unique operating modes for microgrids are grid-connected mode and islanded (autonomous) mode [2], [3]. Different techniques can be used to control distributed energy resources (DER) in microgrids in each mode of operation. DERs based on power electronic converters are typically a microgrid's most important component. In grid-connected mode, the microgrid functions as a current controller and injects power into the main grid by local load and power generation and appropriate market policies [4]. Due to disturbances, such as a breakdown and the subsequent switching occurrences that result from it, preplanned, or resource shores, and due to remoteness, the microgrid may enter an islanded or autonomous mode. Microgrid functions as a voltage controller in islanded operation and oversees voltage regulation, as well as power sharing and balancing. The purpose of power-sharing features is to make sure that each module distributes the load by its rating and the amount of power that is available from its energy source. Grid-forming (master) power converters are always necessary for converters in the islanded mode. Otherwise, there would be no control to maintain the power balance and no voltage control [5]. Due to penetration levels of renewable energy use in the power grid, the old, centralized network faced numerous technical challenges in preserving security and stability by which power quality become poorer [6]. Also due to the non-linear load, there is a disturbance, and harmonics generation in the system occurs which is affect the power quality of the system. To reduce voltage and current harmonics, sag, and swell there are many FACT devices which are shown in Fig.1 [7], [8].

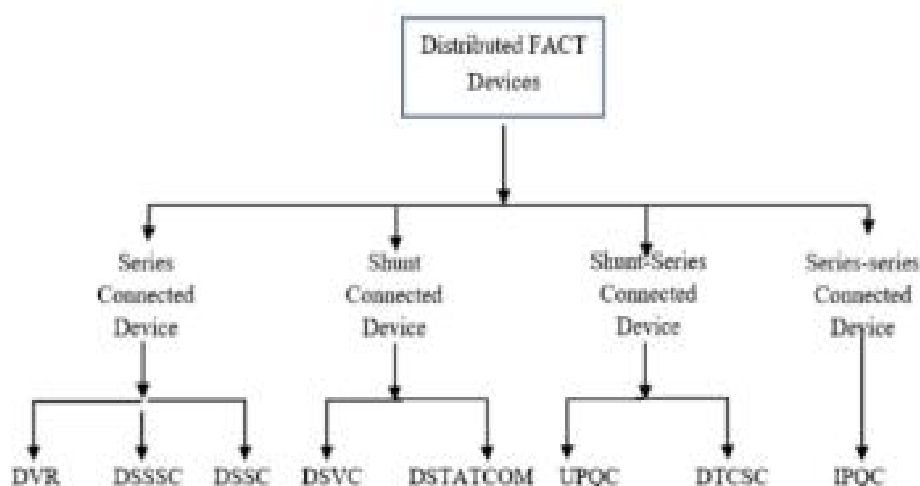


Fig. 1. Distributed FACT devices

In this study, comparison to DVR and DSTATCOM of the same MATLAB Simulink model perform. The DVR is connected in series with the system because it is a series-connected device whereas DSTATCOM connected in parallel with the system. After all, it is shunt-connected device. Both devices are connected to the LV side of the transformer.

TOPOLOGY OF DVR AND DSTATCOM

Due to non-linear loads and complex power electronic devices, there is the majority of automated systems are extremely susceptible to problems with voltage-related power quality, such as harmonics, voltage sag, and swell. To overcome these problems there are many devices in which DVR and DSTATCOM are the most popular to improve power quality problems. Both of the devices use PI-based controller to control the power quality-related problem [9].

A. Structure of DVR

The structure of DVR is shown in Fig. 2, it consists of DC power sources, a Voltage source converter, an Injection transformer, and a Control unit. A power electronic controller used in a DVR protects sensitive electric loads against power quality problems including voltage droop and swell and imbalance issues [10].

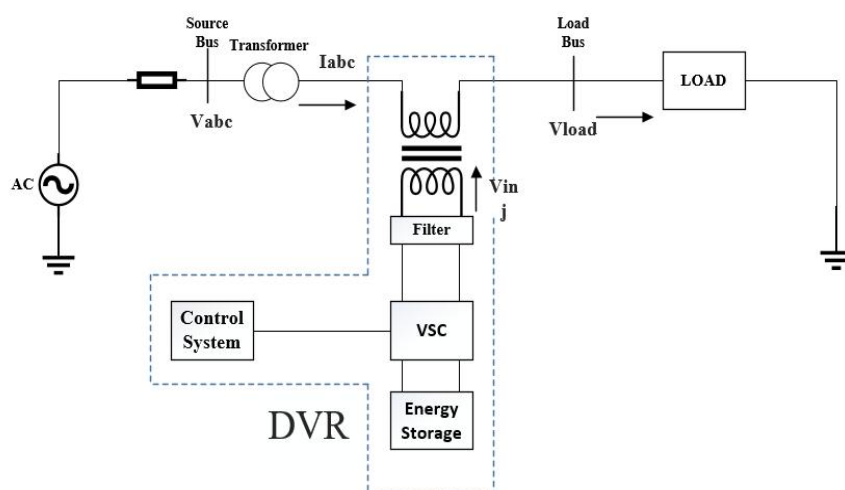


Fig. 2. Structure of DVR

B. Controlling of DVR

The control scheme of DVR is proportional-integral (PI) based used, it calculates the error, which is the difference between the actual output that was detected and the desired set-point, as well as the integral of that value and known as a weighted sum of the error.

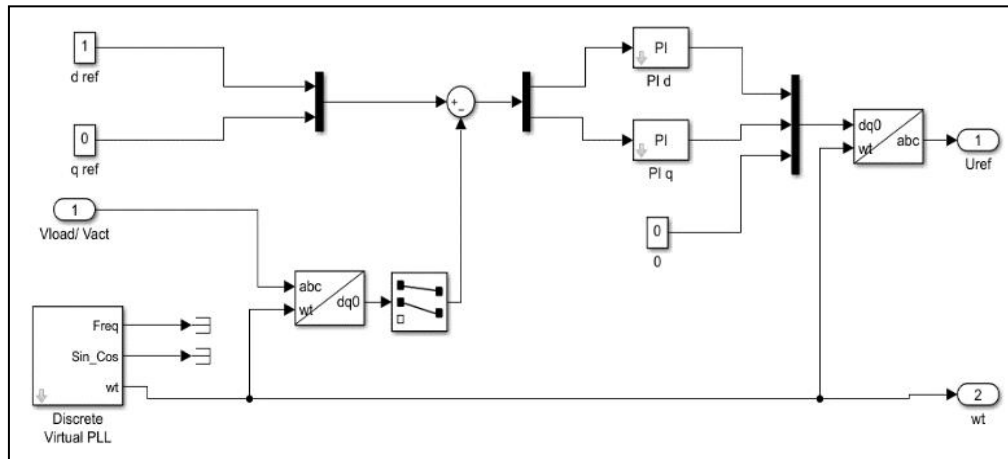
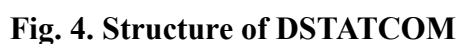


Fig. 3. PI control Simulink of DVR

The steady-state error for a step input is zero thanks to the PI controller's integral term. The difference between V_{ref} and V_{abc} , as depicted in the PI controller's schematic picture, is the actuating input signal. The desired firing sequence is produced by comparing the controller output at the PWM signal generator. The PI-based controller of DVR have three main parts i.e. the PI control unit, abc-dq0 transform, and discrete virtual PLL [11].

C. Structure of DSTATCOM

A STATCOM is a device with a solid-state VSI architecture that is also required for VAR compensation. This is a power electronics-based shunt device that uses insulated gate bipolar transistors and gate-turn-off thyristors to control the power flow. It is referred to as DSTATCOM when this STATCOM is utilized in the distribution system to regulate reactive power. The structure of DSTATCOM is shown in Fig. 4.



D. Controlling of DSTATCOM

The diagram illustrates a closed-loop control system for a three-phase inverter. The components and their interconnections are as follows:

- Reference Voltage:** A constant reference voltage V_{ref} is provided as input to the summing junction.
- Summing Junction:** The reference voltage V_{ref} is subtracted from the feedback voltage to produce the error signal.
- PI Controller:** The error signal is processed by a Proportional-Integral (PI) controller to generate the reference inverter voltage V_{inv_ref} .
- Data Block:** The reference voltage V_{inv_ref} is stored in a data block, which also receives feedback from the inverter output.
- Discrete-Time Block:** The reference voltage is converted to a discrete-time signal using a block labeled $Dis2$.
- Pulse Generator:** The discrete-time signal is converted into a pulse-width modulated (PWM) signal, represented by a block labeled 1 and $Pulses$.
- Three-Phase Inverter:** The PWM signal drives a three-phase inverter, which produces the output voltage V_{abc} .
- Load:** The output voltage V_{abc} is connected to a load, represented by a block labeled $Vload$.
- Feedback:** The output voltage V_{abc} is measured and fed back to the summing junction and the data block.

Fig. 5. PI control Simulink of DSTATCOM

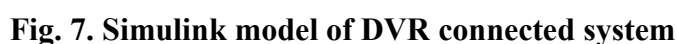


The PI controller in adaptive PI control decreased mistakes and also self-adjusted the PI control gains when the operational parameters were modified. In Fig. 5. this PI control diagram displays a current regulator in the inner loop and a voltage regulator in the outer loop. First, took the reference voltage from the outside loop and measured the bus voltage. After that, the inner and outer loop's proportional and integral parameters are changed. The permissible voltage error (K_d) is tuned to zero, and the outer loop values are tuned to 1.0 in this controller. [13], [14].

SIMULATION AND RESULTS

E. Simulation Model of Proposed System

DVR and DSTATCOM MATLAB Simulink model connected with the 3-phase system is shown in Fig. 7 and Fig. 9 respectively



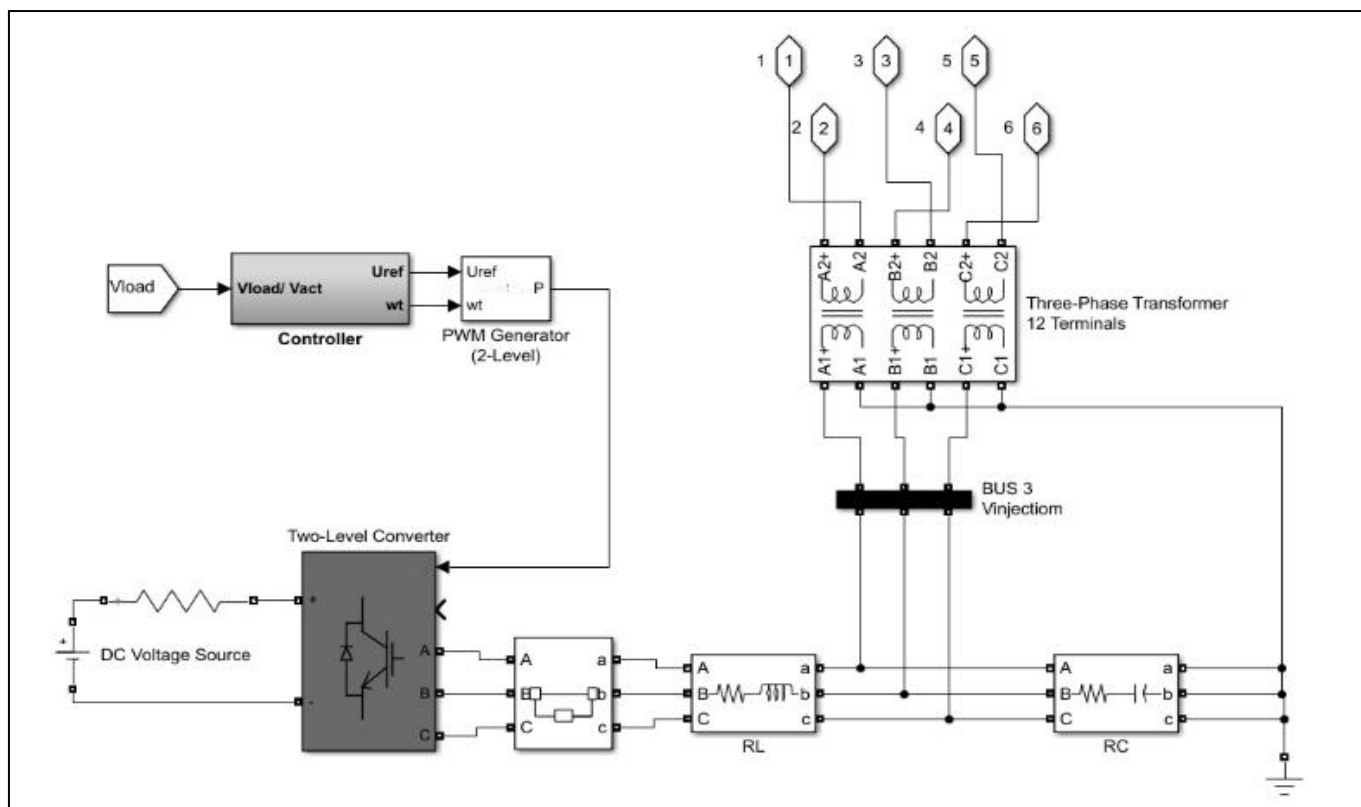


Fig. 8. Simulink model of DVR

In the DVR-connected system shown in Fig. 7 DVR is connected serially with the load bus and source bus. Whereas in Fig. 9 DSTATCOM is connected in parallel with the source bus and load bus.

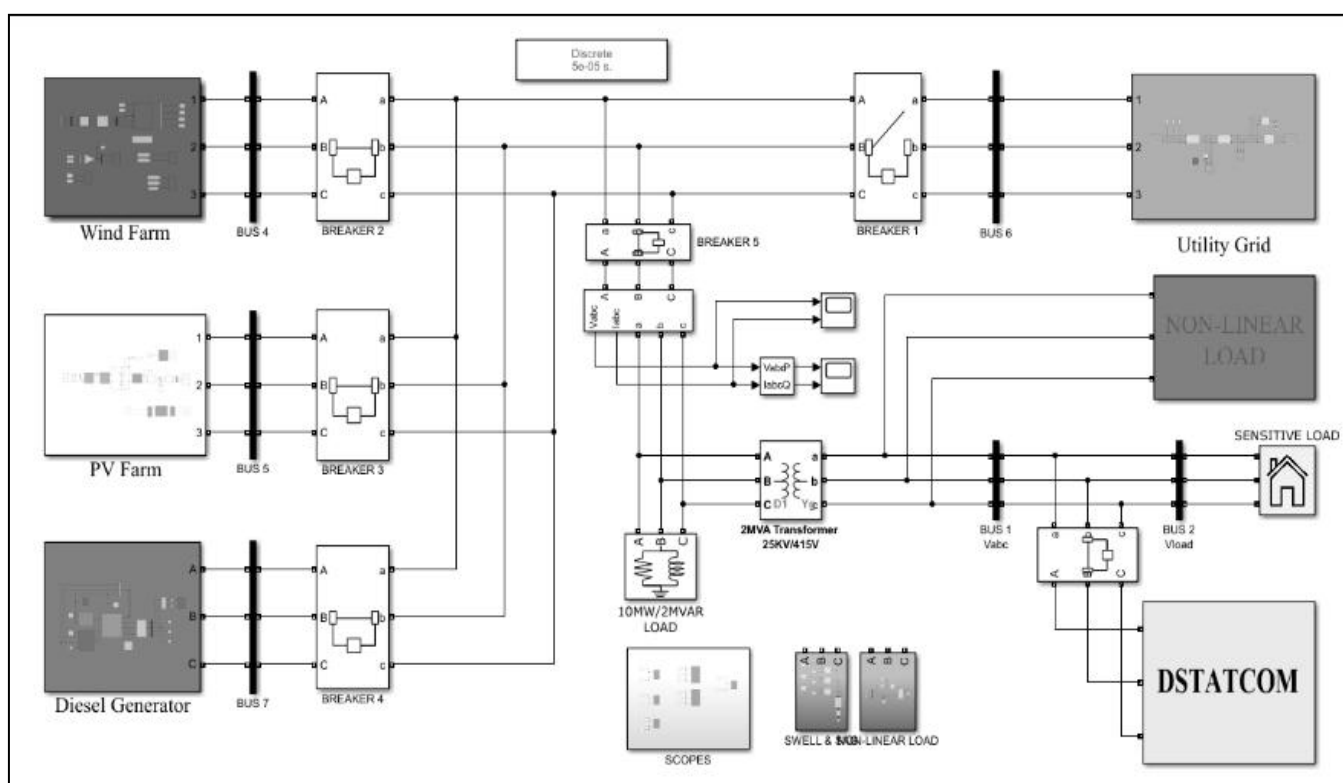


Fig. 9. Simulink model of DSTATCOM connected system

The simulation test has been conducted on both devices DVR and DSTATCOM on the same model to

analyse the harmonic reduction and THD (Total harmonic distortion) improvement which is generated due to connecting non-linear load on the system for the duration of 0.1 to 0.25 sec.

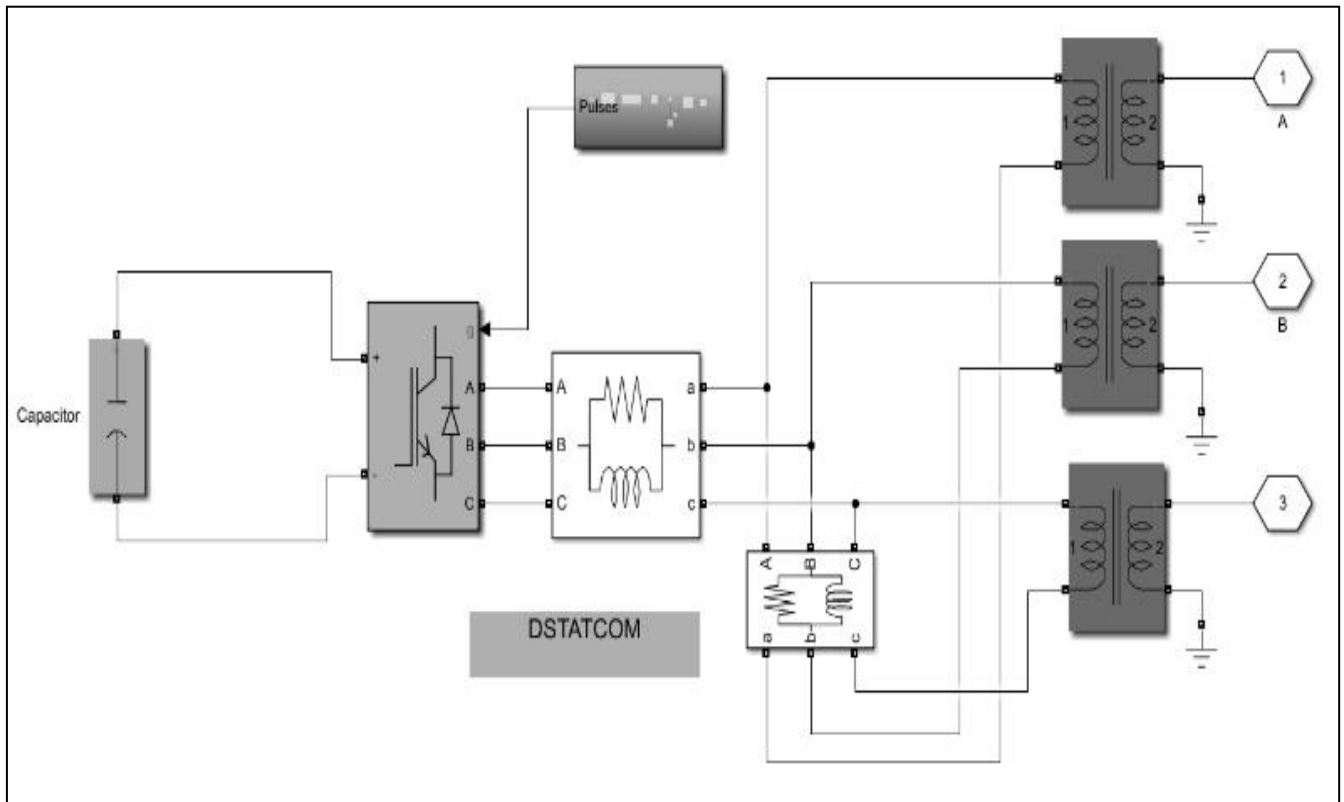
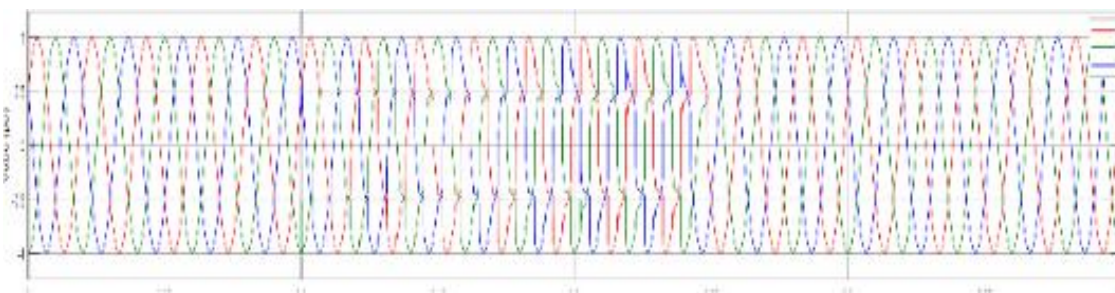


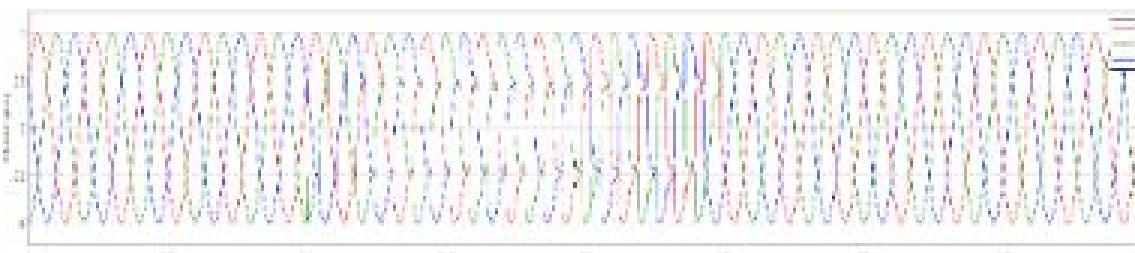
Fig. 10. Simulink model of DSTATCOM

F. Result Obtained Without Using DVR and DSTATCOM

The developed model of microgrid has been simulated with nonlinear load without any FACT device and following results are obtained.



(a)



(b)

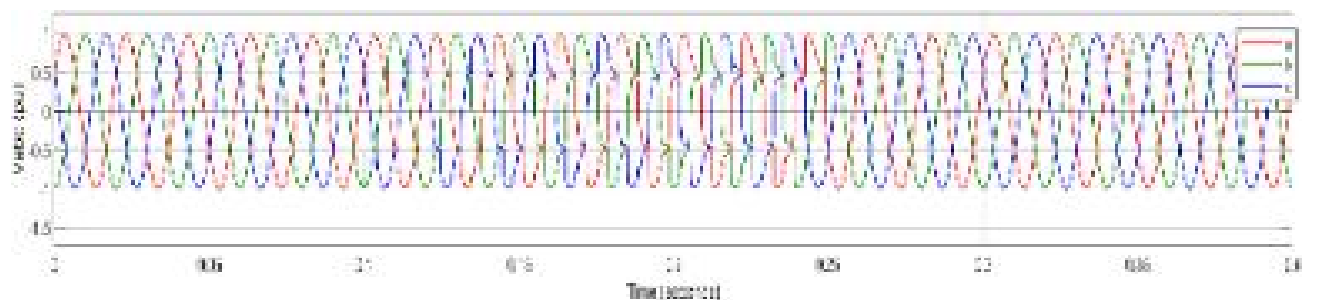
Fig. 11. Waveform without using DVR and DSTATCOM (a) Source voltage bus-1 (b) Load voltage

bus2

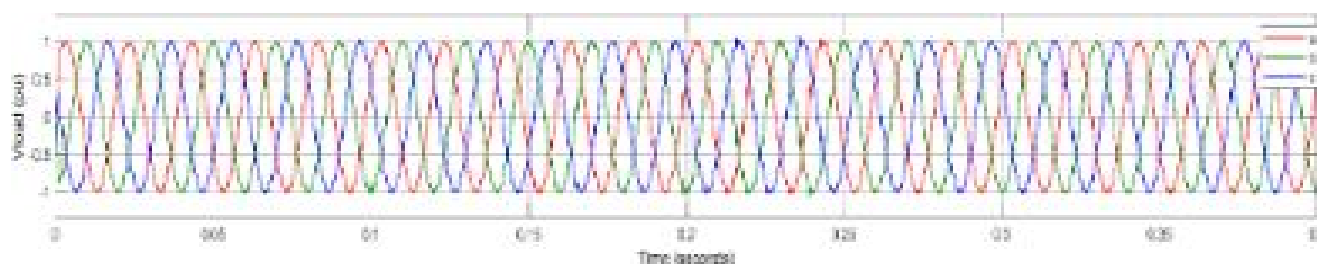
From the above waveform when non-linear load is connected the harmonics generated in the waveform.

G. Result Obtained by Using DVR

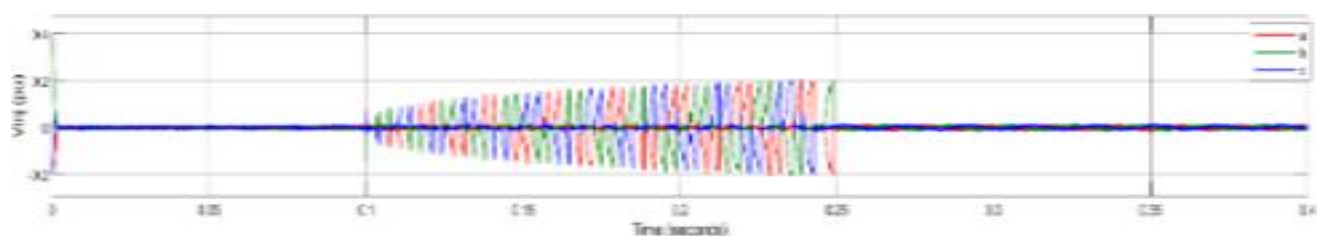
When a non-linear load is connected for the duration of 0.1 to 0.25 sec the following results were found in DVR connected 3-phase system.



(a)



(b)



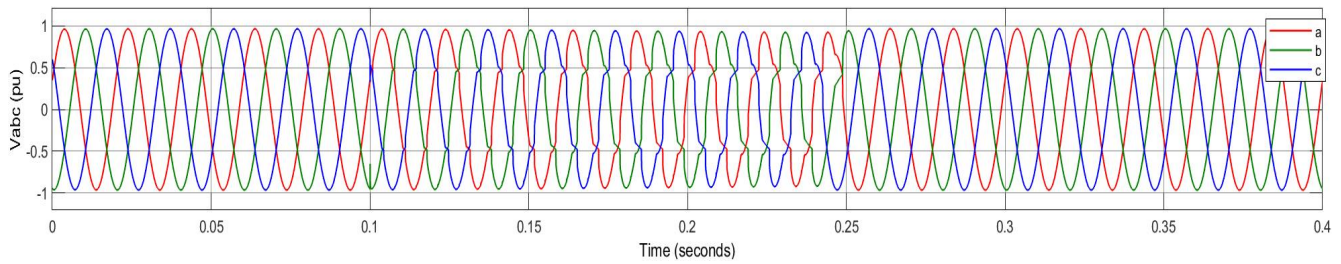
(c)

Fig. 12. (a) V_{abc} voltage at bus-1 (b) V_{load} voltage at load bus-2 (c) V_{inj} Voltage injected by DVR

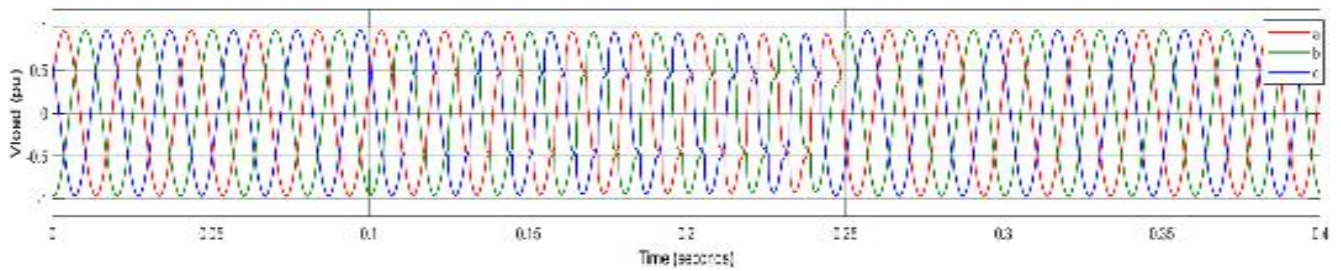
From the above waveforms, the voltage at the load V_{load} side is more uniform as compared to the source side voltage V_{abc} . It is observed that harmonics are reduced by using DVR.

H. Result Obtained by Using DSTATCOM

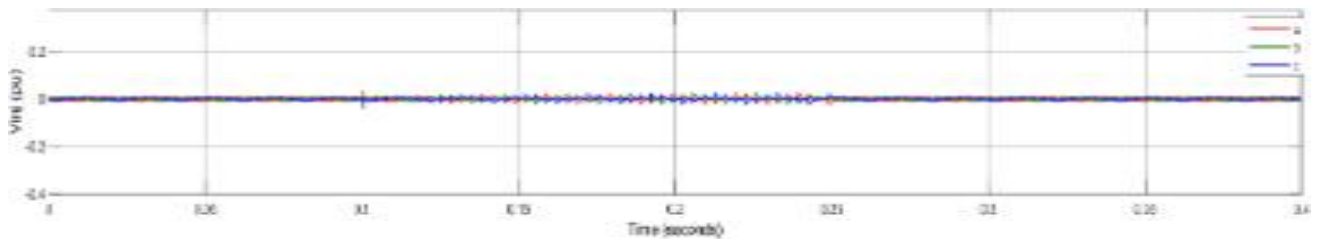
When a non-linear load is connected for the duration of 0.1 to 0.25 sec the following results were found in DSTATCOM connected 3-phase system.



(a)



(b)



(c)

Fig. 13. (a) V_{abc} voltage at bus-1 (b) V_{load} voltage at load bus-2 in DSTATCOM connected system

From the above waveforms, the voltage at the load V_{load} side is more uniform as compared to the source side voltage V_{abc} . It is observed that harmonics are reduced by using DSTATCOM.

TOTAL HARMONIC DISTORTION (THD) ANALYSIS

According to IEEE standard 519 from 1992, the total harmonic distortion is a limit below 5% for the voltage level 69KV or below.

The Total Harmonic DISTORTION (THD) for the non-linear connected systems without and with DVR and DSTATCOM is represented below.

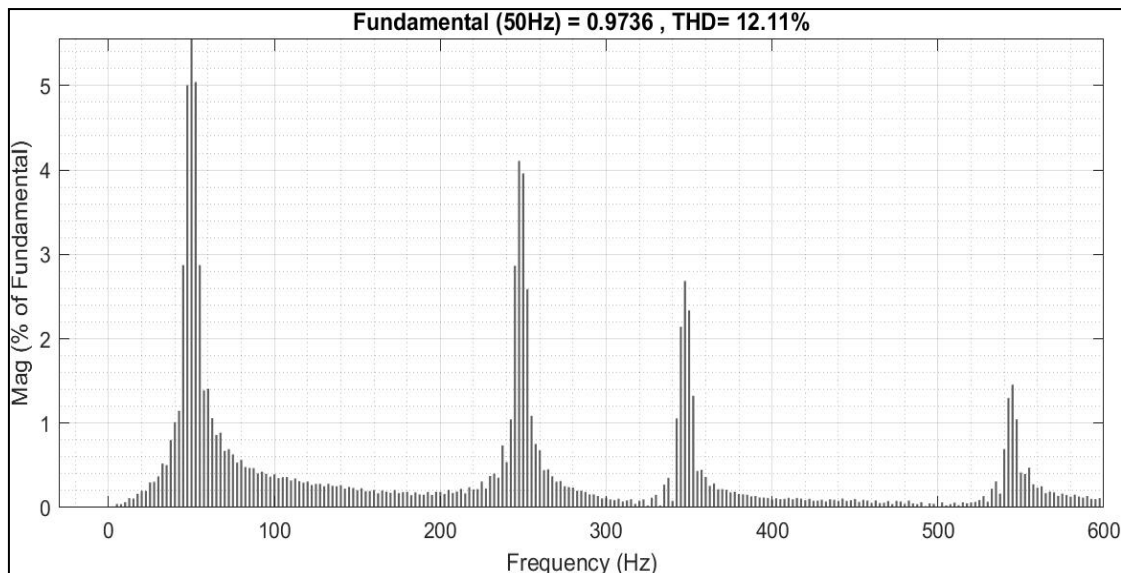


Fig.14. THD of the system without DVR and DSTATCOM

Fig. 14 THD of the system without DVR and DSTATCOM represent the THD of the system without DVR and DSTATCOM is very high 12.11% with is not in the range of IEEE standard.

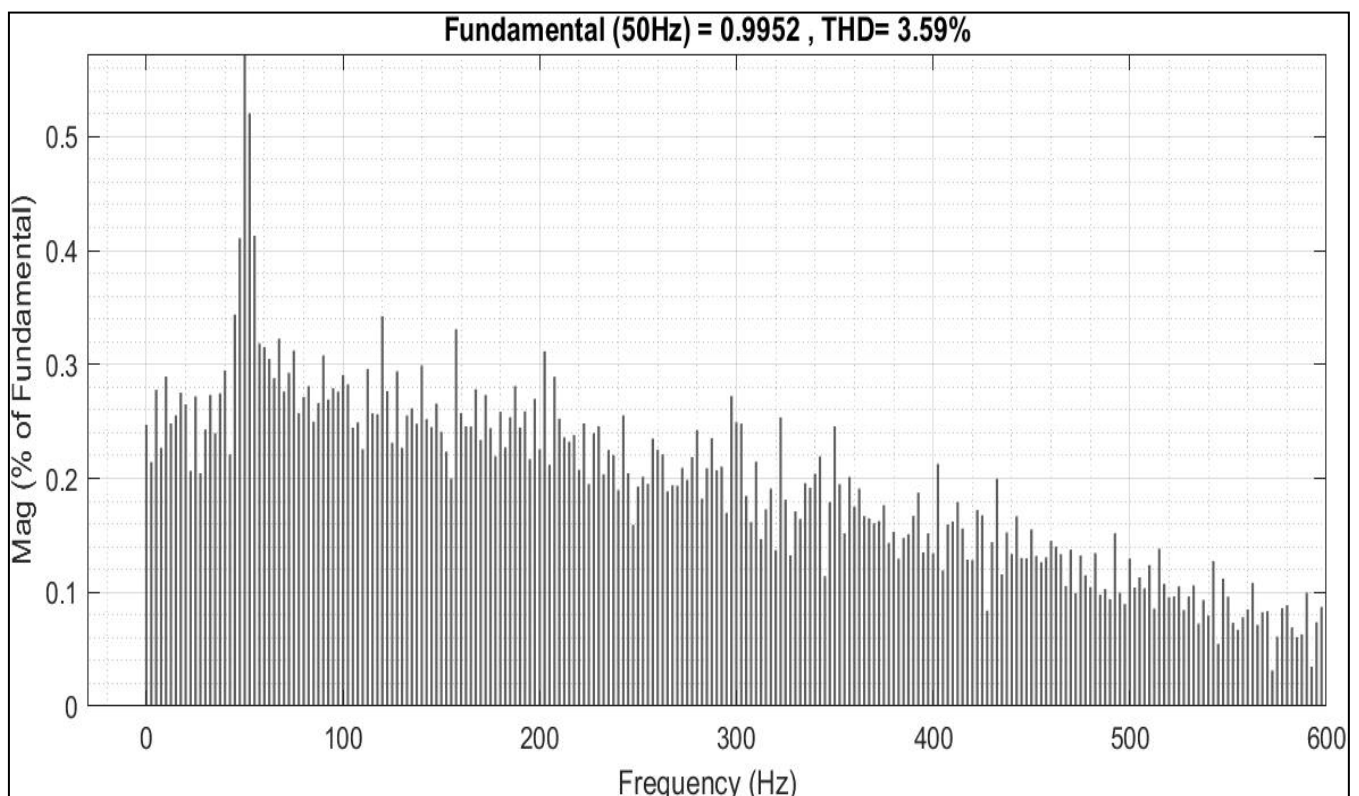


Fig.15. THD of the system with DVR

Fig. 15 THD of the system with DVR represent the THD of the system with DVR (Dynamic Voltage Restorer) is 3.59% which is in the range of IEEE standard.

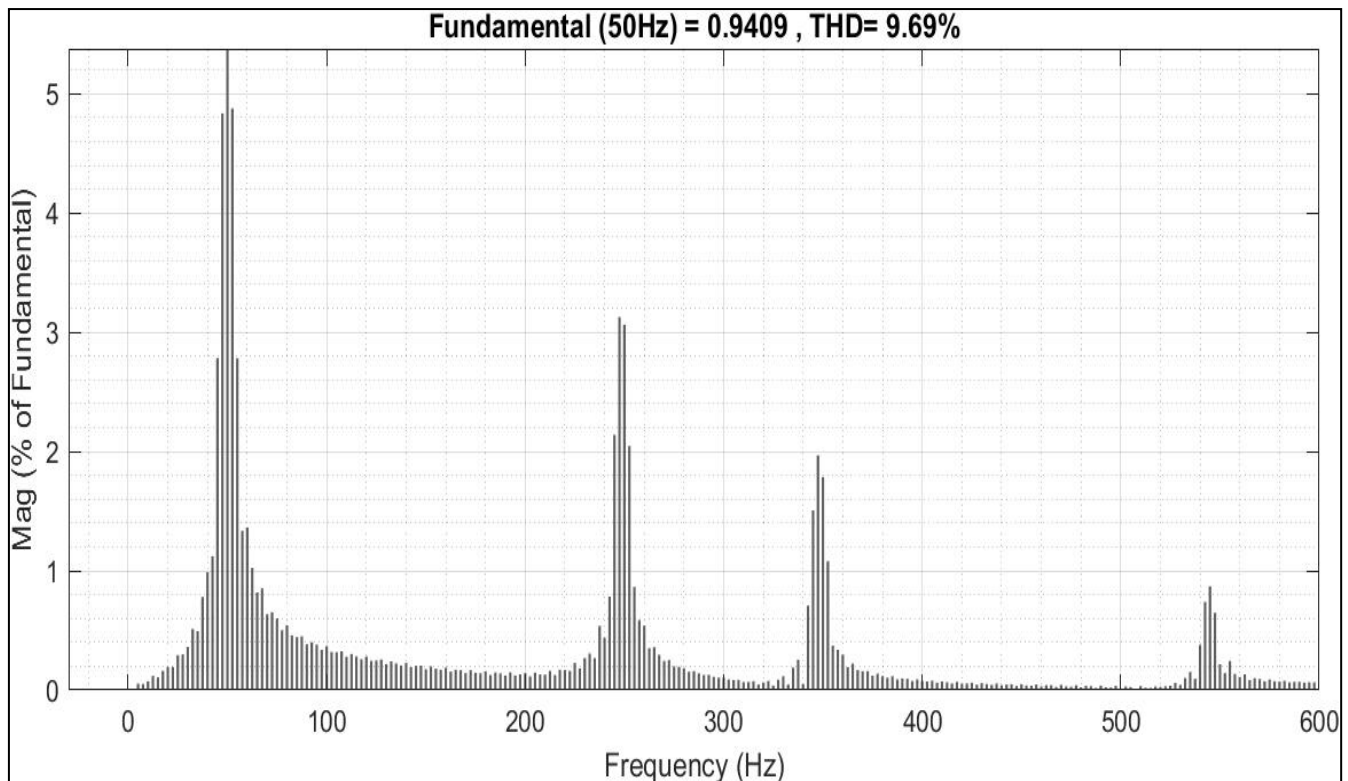


Fig. 16. THD of the system with DSTATCOM

Fig. 16 THD of the system with DSTATCOM represents the THD of the system with DSTATCOM is 9.69% which does not lie the range given in IEEE standards, but it shows the THD improvement of about 2.42%.

ANALYSIS OF TOTAL HARMONICS DISTORTION

Total Harmonic Distortion	
Operation	THD (%)
Without DVR and DSTATCOM	12.11
With DVR	3.59
With DSTATCOM	9.69

CONCLUSION

The MATLAB Simulink platform is used to perform the test analysis of DVR and DSTATCOM on the same Simulink model. By observing and testing both DVR and DSTATCOM, harmonics, and Total harmonic distortion it is found that the performance of DVR is better than the performance of DSTATCOM. The harmonic reduction in the DVR is more as compared to the DSTATCOM. Also, in the THD analysis of both DVR and DSTATCOM it is found that the THD improvement of DVR is better than the DSTATCOM.

REFERENCES

1. M. A. A. Yahya and M. A. R. Uzair, "Performance analysis of DVR, DSTATCOM and UPQC for improving the power quality with various control strategies," 2016 - Bienn. Int. Conf. Power Energy Syst. Towar. Sustain. Energy, PESTSE 2016, 2016, doi: 10.1109/PESTSE.2016.7516483.

2. F. Katiraei, M. R. Iravani, and P. Lehn, "Micro-grid autonomous operation during and subsequent to islanding process," 2004 IEEE Power Eng. Soc. Gen. Meet., vol. 2, no. 1, p. 2175, 2004, doi: 10.1109/pes.2004.1373266.
3. M. Jain, S. Gupta, D. Masand, G. Agnihotri, and S. Jain, "Real-Time Implementation of Islanded Microgrid for Remote Areas," J. Control Sci. Eng., vol. 2016, 2016, doi: 10.1155/2016/5710950.
4. M. Ganjian-Aboukheili, M. Shahabi, Q. Shafiee, and J. M. Guerrero, "Seamless Transition of Microgrids Operation from Grid-Connected to Islanded Mode," IEEE Trans. Smart Grid, vol. 11, no. 3, pp. 2106–2114, 2020, doi: 10.1109/TSG.2019.2947651
5. R. Lasseter et al., "Consortium for Electric Reliability Technology Solutions White Paper on Integration of Distributed Energy Resources The CERTS MicroGrid Concept," Program, Transm. Reliab. Syst. Energy Program, Integr. Interes. Public Comm. Calif. Energy, no. April, pp. 1–29, 2002.
6. U. N. Ekanayake and U. S. Navaratne, "A Survey on Microgrid Control Techniques in Islanded Mode," J. Electr. Comput. Eng., vol. 2020, 2020, doi: 10.1155/2020/6275460.
7. N. Hanani, "Chapter 1 An Autom. Irrig. Syst. Using Arduino Microcontroller, vol. 3, no. May, pp. 2–6, 2018.
8. Y. W. Li, D. M. Vilathgamuwa, P. C. Loh, and F. Blaabjerg, "A dual-functional medium voltage level DVR to limit downstream fault currents," PESC Rec. - IEEE Annu. Power Electron. Spec. Conf., vol. 22, no. 4, pp. 1330–1340, 2006, doi: 10.1109/PESC.2006.1712084.
9. Z. Shuai, P. Yao, Z. J. Shen, C. Tu, F. Jiang, and Y. Cheng, "Design considerations of a fault current limiting dynamic voltage restorer (FCL-DVR)," IEEE Trans. Smart Grid, vol. 6, no. 1, pp. 14–25, 2015, doi: 10.1109/TSG.2014.2357260
10. S. Karare and V. M. Harne, "Modelling and simulation of improved operation of D-STATCOM in distribution system for power quality improvement using MATLAB Simulink tool," Proc. Int. Conf. Electron. Commun. Aerosp. Technol. ICECA 2017, vol. 2017-Janua, no. Icesa, pp. 346–350, 2017, doi: 10.1109/ICECA.2017.8212831.
11. I. Syed and V. Khadkikar, "A Dynamic Voltage Restorer (DVR) based interface scheme for microgrids," IECON Proc. (Industrial Electron. Conf., pp. 5143–5149, 2014, doi: 10.1109/IECON.2014.7049283.
12. B. P. Ganthia, S. Mohanty, P. K. Rana, and P. K. Sahu, "Compensation of voltage sag using DVR with PI controller," Int. Conf. Electr. Electron. Optim. Tech. ICEEOT 2016, pp. 2138–2142, 2016, doi: 10.1109/ICEEOT.2016.7755068.